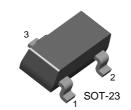


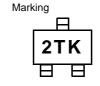
November 2006

# **MMBT4403K**

# **PNP Epitaxial Silicon Transistor**

# **Switching Transistor**





1. Base 2. Emitter 3. Collector

## Absolute Maximum Ratings $T_a = 25$ °C unless otherwise noted

Symbol	Parameter	Value	Units	
V <sub>CBO</sub>	Collector-Base Voltage	-40	V	
$V_{CEO}$	Collector-Emitter Voltage -40 V		V	
$V_{EBO}$	Emitter-Base Voltage	-5	V	
I <sub>C</sub>	Collector Current -600 mA		mA	
P <sub>C</sub>	Collector Power Dissipation 350 mW			
T <sub>J,</sub> T <sub>STG</sub>	Operating Junction and Storage Temperature Range -55 ~ 150 °C			

# Electrical Characteristics T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = -0.1 \text{mA}, I_E = 0$	-40		V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage *	$I_C = -1.0 \text{mA}, I_B = 0$	-40		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = -0.1 \text{mA}, I_C = 0$	-5		V
I <sub>BL</sub>	Base Cut-off Current	$V_{CE} = -35V, V_{EB} = -0.4V$		-0.1	μΑ
I <sub>CEX</sub>	Collector Cut-off Current	V <sub>CE</sub> = -35V, V <sub>EB</sub> = -0.4V		-0.1	μΑ
h <sub>FE</sub>	DC Current Gain	$V_{CE} = -1V$ , $I_{C} = -0.1$ mA $V_{CE} = -1V$ , $I_{C} = -1.0$ mA $V_{CE} = -1V$ , $I_{C} = -10$ mA $V_{CE} = -2V$ , $I_{C} = -150$ mA * $V_{CE} = -2V$ , $I_{C} = -500$ mA *	30 60 100 100 20	300	
V <sub>CE (sat)</sub>	Collector-Emitter Saturation Voltage *	I <sub>C</sub> = -150mA, I <sub>B</sub> = -15mA I <sub>C</sub> = -500mA, I <sub>B</sub> = -50mA		-0.4 -0.75	V V
V <sub>BE (sat)</sub>	Base-Emitter Saturation Voltage *	I <sub>C</sub> = -150mA, I <sub>B</sub> = -15mA I <sub>C</sub> = -500mA, I <sub>B</sub> = -50mA	-0.75	-0.95 -1.3	V V
f <sub>T</sub>	Current Gain Bandwidth Product	$I_C = -20 \text{mA}, V_{CE} = -10 \text{V}, f = 100 \text{MHz}$	200		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = -10V, I <sub>E</sub> = 0, f = 140KHz		8.5	pF
t <sub>ON</sub>	Turn On Time	V <sub>CC</sub> = -30V, V <sub>BE</sub> = -2V I <sub>C</sub> = -150mA, I <sub>B1</sub> = -15mA		35	ns
t <sub>OFF</sub>	Turn Off Time	$V_{CC} = -30V, I_C = -150mA$ $I_{B1} = I_{B2} = -15mA$		255	ns

<sup>\*</sup> Pulse Test: Pulse Width $\leq$ 300 $\mu$ s, Duty Cycle $\leq$ 2%

## **Typical Performance Characteristics**

Figure 1. DC current Gain

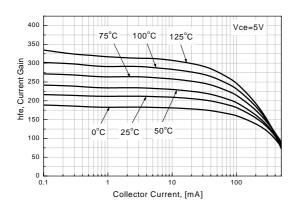


Figure 2. Collector-Emitter Saturation Voltage

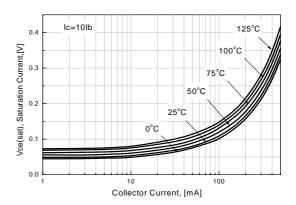


Figure 3. Base-Emitter Saturation Voltage

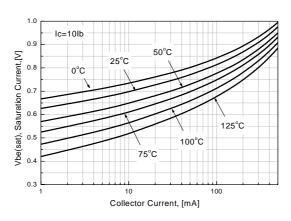


Figure 4. Collector - Base Leakage Current

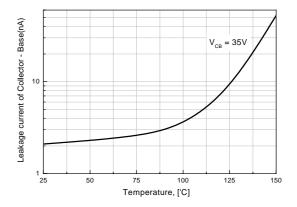


Figure 5. Collector-Base Capacitance

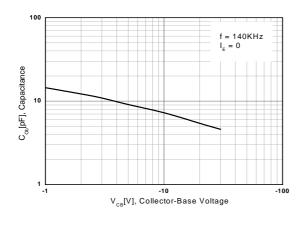
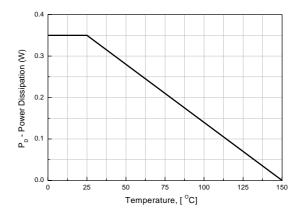


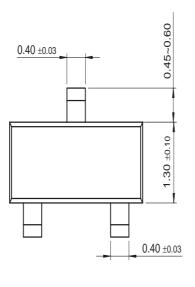
Figure 6. Power Dissipation vs
Ambient Temperature

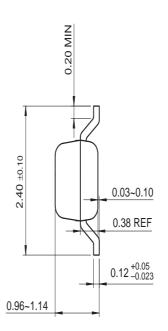
2

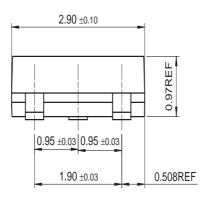


## **Mechanical Dimensions**

# SOT-23







Dimensions in Millimeters

UltraFET<sup>®</sup>

UniFET™

 $VCX^{TM}$ 

 $\mathsf{Wire}^{\scriptscriptstyle\mathsf{TM}}$ 

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